

Original Article

# Error Detection Technique for a Median Filter

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**Abstract:** In picture legal sciences, recognition of picture imitations including non-direct controls have gotten a lot of interest in late past. Middle separating (MF) is one such non-straight control procedure which is regularly utilized in number of uses, for example, to conceal drive commotions. Notwithstanding, a SRAM-based processor execution of this channel is then defenseless to arrangement memory cycle flips actuated by memory defects, so a security strategy is required for basic process in which the appropriate channel activity should be guaranteed. A grouping for their portrayal is presented. To produce a test calculation for recognition of the relative multitude of considered issues, it was shown that it's anything but a simple issue. For this reason, another design situated walk calculation technique is created. an issue lenient execution of the middle channel is introduced and considered inside and out. Our security strategy checks assuming the middle result is inside a powerful reach made with the excess non-middle results. A result mistake signal is enacted on the off chance that a defiled picture pixel is distinguished, an incomplete or finish reconfiguration can be processed to eliminate the error. The proposed error recovery technique coded in Verilog and verified using simulator.

**Keywords:** Median Filter, Error Detection.

## INTRODUCTION

Clamor is any undesired data that sullies a picture. Commotion [1] shows up in a picture from an assortment of sources. The Salt and Pepper type noise is considered as a basic noise in the image. The median value of the pixels is used to indicate a noise. The entire noises a preprocessed to remove the noise for image enhancement.

The functioning methodology of the current mean sifting strategy is extremely straightforward. For the current mean separating procedure each pixel is taken in turn and surrounding pixels are considered. The unseated pixels produces different pixel values due to the memory upsets.

In this work, the march test algorithm is applied for MF for fault detection. The rest of work contributed as: section 2 discusses the basic MF function and proposed work presented in section 3. The result and discussion is described in section 4 and concluded in section 5.

## BASIC MEDIAN FILTER FUNCTION

This section is presented a basic process of median filtering. The Figure 1 outlines the depicted middle replacement process with a 3x3 pixels square window. It ought to be noticed that the dissected "dead pixel" is eliminated from the first picture.

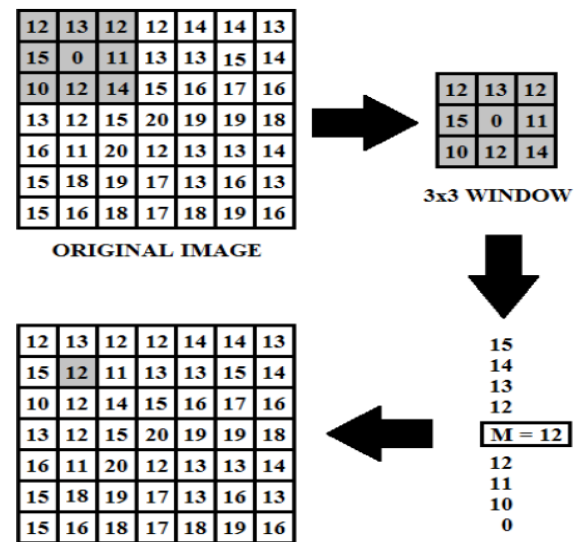


Fig. 1 Median process

The dead cell is marked as 0 which can be evaluated using its neighbourhood pixels. For example, the 3x3 matrix pixel is taken and evaluated. The descending order sorting is carried to select the median value. Finally the median value is replaced for a dead cell.

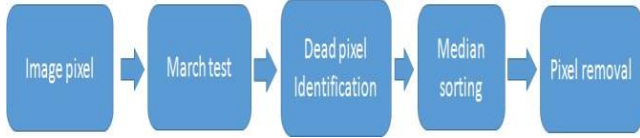
## PROPOSED ALGORITHM

This section presented a Fault-lenient execution of the middle channel is carried out utilizing proposed walk evaluation. The security method monitors on the off chance



that the middle result is inside a unique reach made with the excess non-middle results.

A result mistake signal is enacted on the off chance that a tainted picture pixel is recognized, a fractional or finish performed a reconfiguration to eliminate the error in setup memory



**Fig. 2 Proposed Flow Diagram**

#### March test method

This calculation  $S$  is a test calculation with a Marchlimited components  $S = S1; S2; \dots; Sk$ . Every March component  $M_i$  comprises of a tending to arrange  $A_i$  and Read/Write function.

#### Algorithm 1: March Test Algorithm

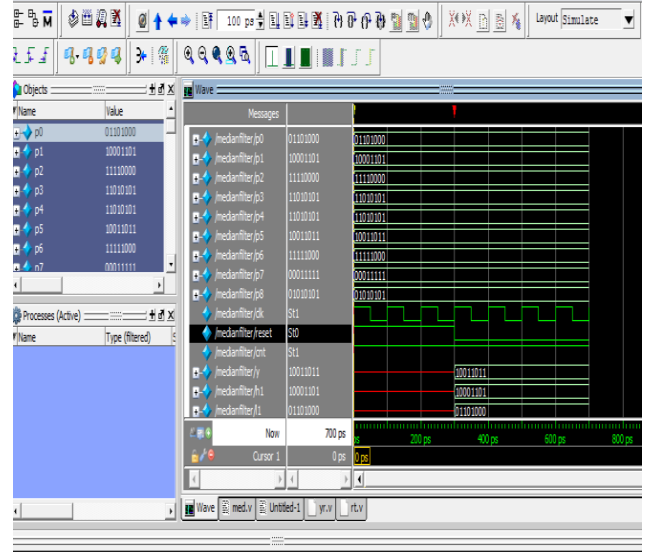
- 1:  $V \leftarrow$  select  $v$  from possible initial states with  $\max(\#(FE_v))$ .
- 2:  $ME \leftarrow \emptyset, AO \leftarrow NULL$
- 3: **repeat**
- 4:  $fe \leftarrow get\_fe(FE_v)$  it chooses one  $fe$  incident from the current state  $V$
- 5: **if** ( $fe = \emptyset$ ) **then**
- 6:  $close\_me(ME)$
- 7:  $print(ME)$
- 8:  $ME \leftarrow \emptyset, AO \leftarrow NULL$
- 9:  $V \leftarrow get\_new\_state()$
- 10: **else**
- 11:  $put\_fe\_in\_me(fe, ME)$
- 12: Update  $V$  with  $fe$  {it determines the new state by moving on the good machine}
- 13: delete  $fe$
- 14: **end if**
- 15: **until** ( $FE_v = \emptyset; \forall v$ )

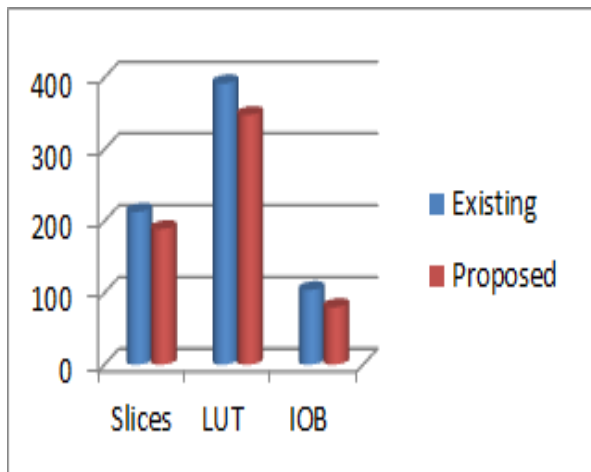
This method accomplishes a low-power, high-throughput, and particular equipment plan of fractional arranging organization. The proposed method is contrasting modules move the files of tests as opposed to moving the info information straightforwardly. Power dispersal is diminished by limiting exchanging exercises and sign

advances. To forestall pointless contrasting of the huge informational index is presented that utilized the minimum power arranging method and a clever cut-out instrument.

## RESULTS AND DISCUSSION

The section discussed and evaluated a proposed method. The figure 3 shows the simulation result and table showed the result of comparison table. The figure 4 presented the performance chart of proposed work.





**Fig.4 Performance Chart**

## CONCLUSION

The proposed method checks on the off chance that the middle result esteem is inside a powerful reach made each time with the excess non-middle results. This march test method is functioned with an efficient area reduction. The faults are easily evaluated using this proposed method. By comparison, the proposed march test method occurred a minimum number of area, delay and power than the existing.

## REFERENCES

- [1] H.-L. Eng, and K.-K. Ma, "Noise adaptive soft-switching median filter," *IEEE Trans. Image Process.*, vol. 10, no. 2, pp. 242-251, 2001.
- [2] B. Jahne, "Applications and Tools," in *Digital Image Processing*, 6th ed. Berlin, Germany: Springer, 1991, ch. 1, sec. 3, pp. 14-16.
- [3] R. Szeliski, "Image processing," in *Computer Vision: Algorithms and Applications*, Springer. Science & Business Media, 2010, ch. 3, sec. 2-3, pp. 111-127.
- [4] L. Sterpone, M. S. Reorda, M. Violante, F. L. Kastensmidt, and L. Carro, "Evaluating different solutions to design fault tolerant systems with SRAM-based FPGAs," *Journal of Electronic Testing*, vol. 23, no. 1, pp. 47-54, 2007.
- [5] G. R. Hopkinson, "Radiation effects in a CMOS active pixel sensor," *IEEE Trans. Nucl. Sci.*, vol. 47, no. 6, pp. 2480-2484, 2000.
- [6] M. Bagatin, "Introduction to the Effects of Radiation on Electronics Devices," in *Ionizing Radiation Effects in Electronics*. 1st ed. Boca Raton, Florida, ch.1, sec. 1.4, pp. 15-16, 2016.
- [7] A. Bovik, "Handbook of Image and Video Processing". New York: Academic, 2000.
- [8] B. Shim, and N. R. Shanbhag, "Reduced precision redundancy for low-power digital filtering," in *Conference Record of the Thirty-Fifth Asilomar Conference on Signals, Systems and Computers*, Pacific Grove, CA, pp. 148-152 vol. 1, 2001.
- [9] P. Reviriego, J. A. Maestro, I. L'opez-alle, and J. A. de Agapito, "Soft Error Tolerant Infinite Impulse Response Filters Using Reduced Precision Replicas", in *Proc. of the Radiation Effects on Components and Systems (RADECS) conference*, Sevilla, Spain, pp. 493-496, 2011.
- [10] L. A. Aranda, P. Reviriego, and J. A. Maestro, "A Fault-Tolerant Implementation of the Median Filter", presented at the *Radiation Effects on Components and Systems (RADECS) Conference*, Bremen, Germany, 2016.
- [11] L. Yin, R. Yang, M. Gabbouj and Y. Neuvo, "Weighted median filters: a tutorial," *IEEE Trans. Circuits Syst. II, Analog Digit. Signal Process.*, vol. 43, no. 3, pp. 157-192, 1996.
- [12] M. A. Vega-Rodríguez, J. M. Sánchez-Pérez, and J. A. Gómez-Pulido, "An FPGA-based implementation for median filter meeting the realtime requirements of automated visual inspection systems," *Proc. 10<sup>th</sup> Mediterranean Conf. Control and Automation*, Lisbon, Portugal, 2002.
- [13] K. Benkrid, D. Crookes, and A. Benkrid, "Design and implementation of a novel algorithm for general purpose median filtering on FPGAs," *IEEE International Symposium on Circuits and Systems (ISCAS)*, pp. IV-425-IV-428 vol.4, 2002.
- [14] G. L. Bates, and S. Nooshabadi, "FPGA implementation of a median filter," *Proceedings of IEEE Speech and Image Technologies for Computing and Telecommunications Conference (TENCON)*, Brisbane, Qld., pp. 437-440 vol.2, 1997.
- [15] S. A. Fahmy, P. Y. K. Cheung, and W. Luk, "Novel FPGA-based implementation of median and weighted median filters for image processing," *International Conference on Field Programmable Logic and Applications*, Tampere, Finland, pp. 142-147, 2005.